

SW CIRCUIT

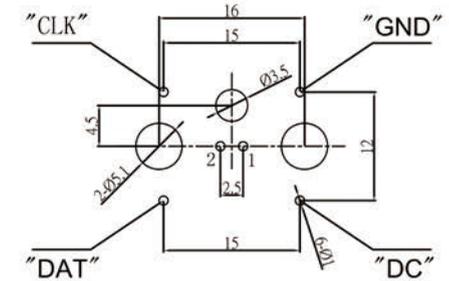
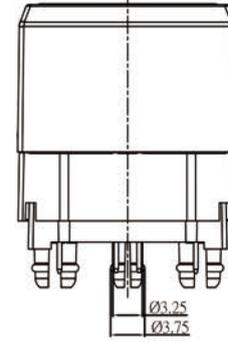
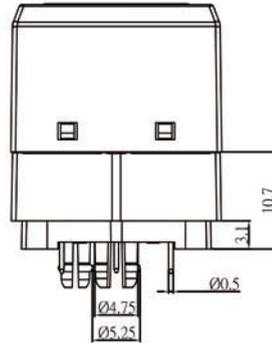
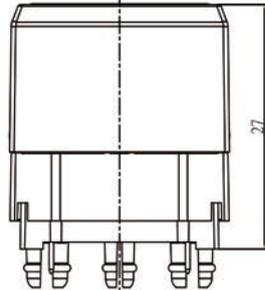
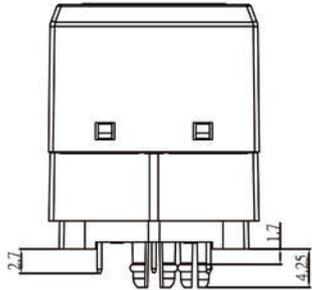
"RGB"

BACKLIGHT

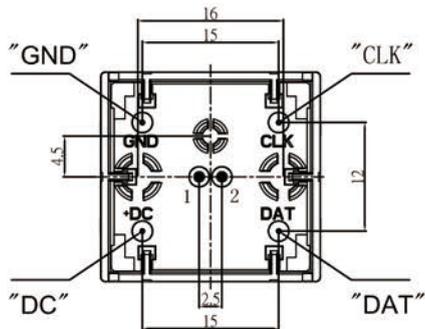
"SPI"

INTERFACES

DIMENSION	TOLERANCE
≤10	± 0.3
10~100	± 0.5
≥100	± 0.8



P. C. B. LAYOUT



1. SWITCH(1->2) RATING : 30 V DC , 0.1A MAX.
2. CONTACT RESISTANCE : 200Ω MAX .(Initial)
3. INSULATION RESISTANCE : 100 MΩ MIN(DC 100V) .
4. WITHSTAND VOLTAGE : AC 100 V / 1 MINUTE.
5. STORAGE TEMPERATURE : -25°C ~ 70°C .
6. OPERATING FORCE : 150±30 gf .
7. FULL TRAVEL : 2.0±0.3 .
8. LIFE CYCLE: 3,000,000 CYCLES .
9. 64(horizontal)X32(vertical) Pixel FORMAT
10. ALL pins MUST BE CHECK THE "DSP12 SERIES LCD SPEC"

"1"	SWITCH Terminal
"2"	SWITCH Terminal
"GND"	GND[Gorund]
"DC"	V _{DD} [Power]
"CLK"	CLOCK LINE
"DAT"	COMMAND AND DATA LINE

Terminal's Configuration

UNIT	SCALE	ANGLE TER	VIEW	MODE		
mm	1 : 1	± 3°		PUSH WITH LCD SWITCH		
VER DATE APPROVAL CONFIRM DESIGN ENGINEERING RECORD DESCRIPTION					DSP12-NITFD-ATN	
VO 2010/7/25 JOHNSON ALEN MIKE ORIGINAL DESIGN					Tel: (86) 755-29424722/29424733	