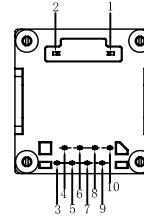


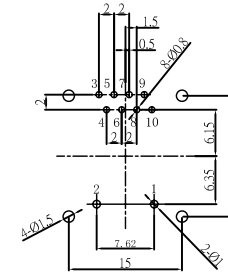
SW CIRCUIT

"64X64"
"OLED"
[RGB]

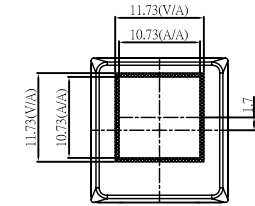
PIXEL FORMAT



Terminal's VIEW

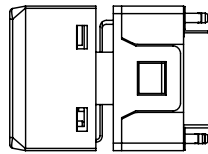
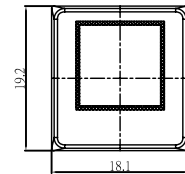
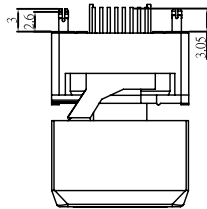


P.C.B. LAYOUT



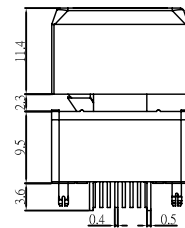
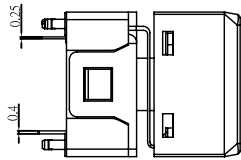
VIEW AREA

DIMENSION	TOLERANCE
≤10	± 0.3
10~100	± 0.5
≥100	± 0.8



PIN 1	SWITCH Terminal
PIN 2	SWITCH Terminal
PIN 3	VCC[+12.8V] [Power Supply for oel panel]
PIN 4	SDIN [Serial Data Input Signal]
PIN 5	SCLK [Serial Clock Input Signal]
PIN 6	D/C# [Data/Command Control]
PIN 7	RES# [Power Reset for Controller and Driver]
PIN 8	CS# [Chip Select]
PIN 9	VDD[+3.3V][Power Supply for Core Logic Circuit]
PIN 10	VSS [Ground of Logic Circuit]

Terminal's Configuration



1. SWITCH(1->2) RATING : 12 V DC , 0.1A MAX.
2. CONTACT RESISTANCE : 200 mΩ MAX .(Initial)
3. INSULATION RESISTANCE : 100 MΩ MIN(DC 250V) .
4. WITHSTAND VOLTAGE : AC 125 V / 1 MINUTE.
5. OPERATING TEMPERATURE : -25°C ~ 70°C .
6. OPERATING FORCE : 180±50 gf .
7. FULL TRAVEL : 1.8±0.3 .
8. LIFE CYCLE: 2,000,000 CYCLES .
9. 64(horizontal)X64(vertical) Pixel FORMAT
10. PIN3~PIN10 MUST BE CHECK THE " OLED SPEC"

11. DISPLAY MODE: PASSIVE MAXTRIX
12. DISPLAY COLOR: 65,536 COLORS(MAXIMUM)
13. DRIVE DUTY: 1/64 DUTY
14. NUMBER OF PIXELS: 64(RGB)X64
15. ACTIVE AREA: 10.73X10.73
16. PIXEL PITCH: 0.056X0.168
17. PIXEL SIZE: 0.034X0.143
18. MODULE SIZE: 15X32.66X1.2
19. PANEL SIZE: 15X17.5X1.2 INCLUDING "ANTI-GLARE POLARIZER"

UNIT	SCALE	ANGLE TER	VIEW	MODE	
mm	1 : 1	± 3°		PUSH WITH OLED SWITCH	WWW.SZLAKEVIEW.COM ELECTRONICS CO., LTD
VER	DATE	APPROVAL	CONFIRM	DESIGN	ENGINEERING RECORD DESCRIPTION
					DSP-N1TF0-CRKT
					Tel: (86)755-29424722/29424733